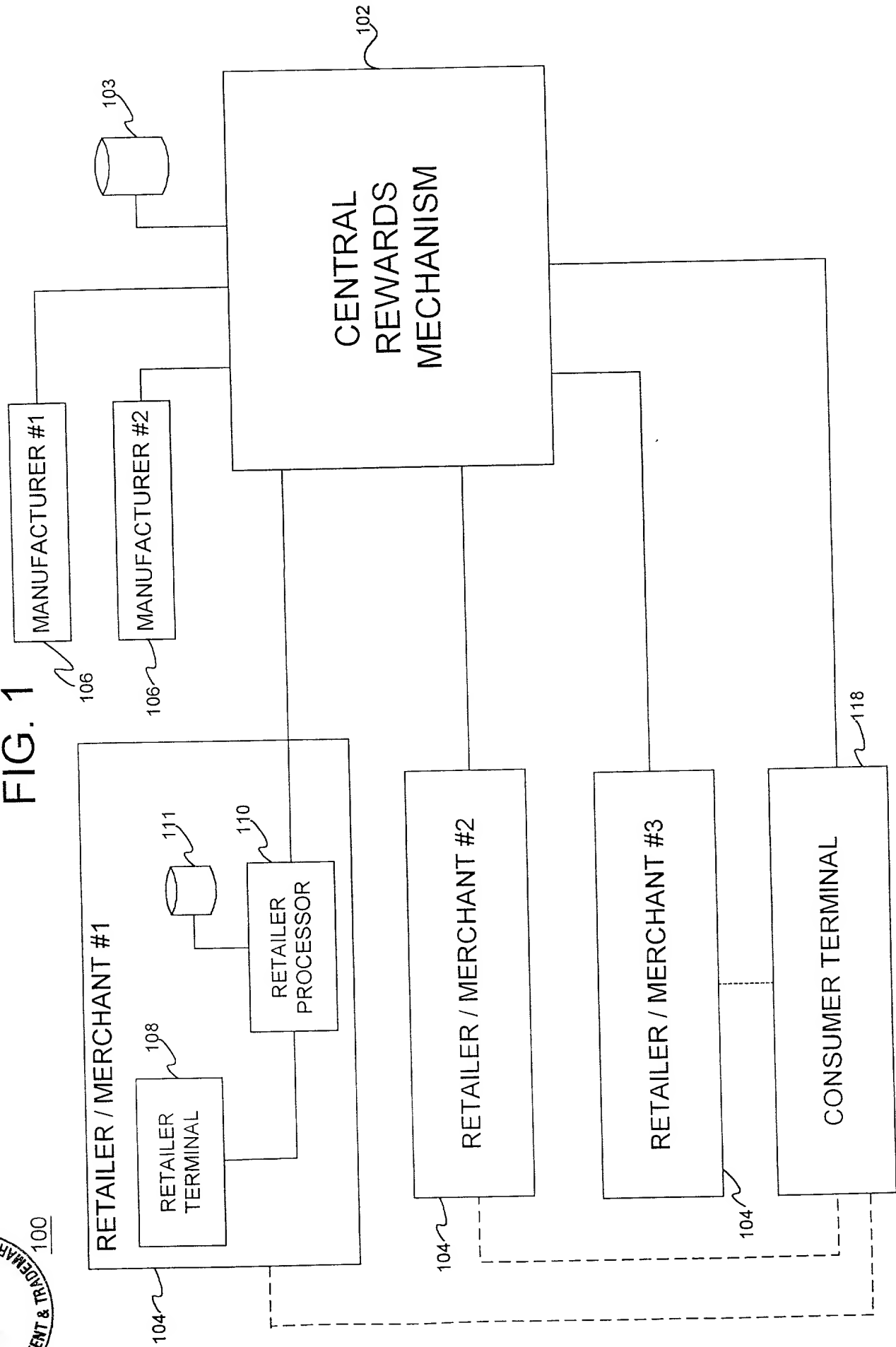


FIG. 1



U.S. PATENT & TRADEMARK OFFICE
AUG 21 2002
11 P E JC180

FIG. 2

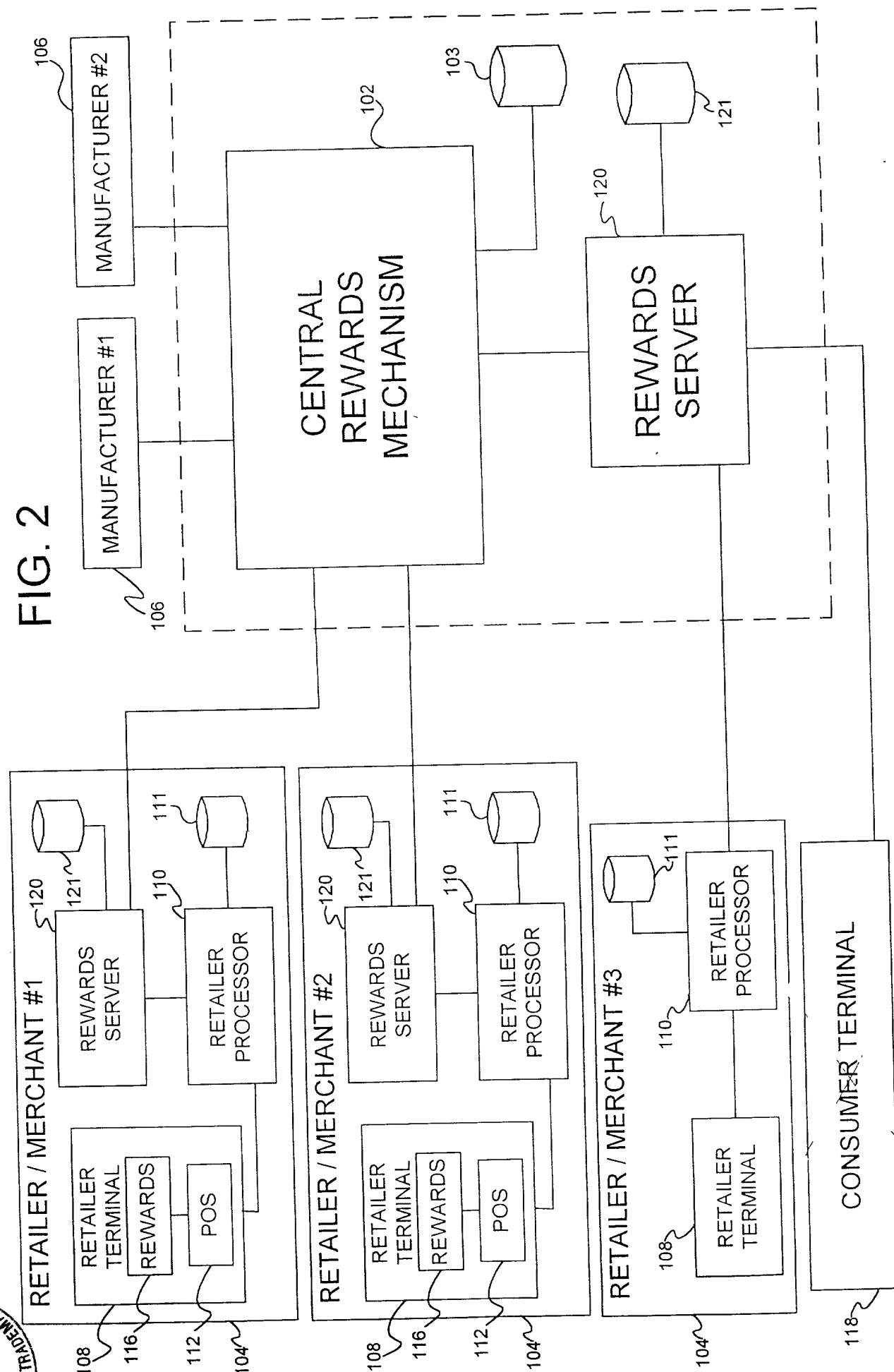
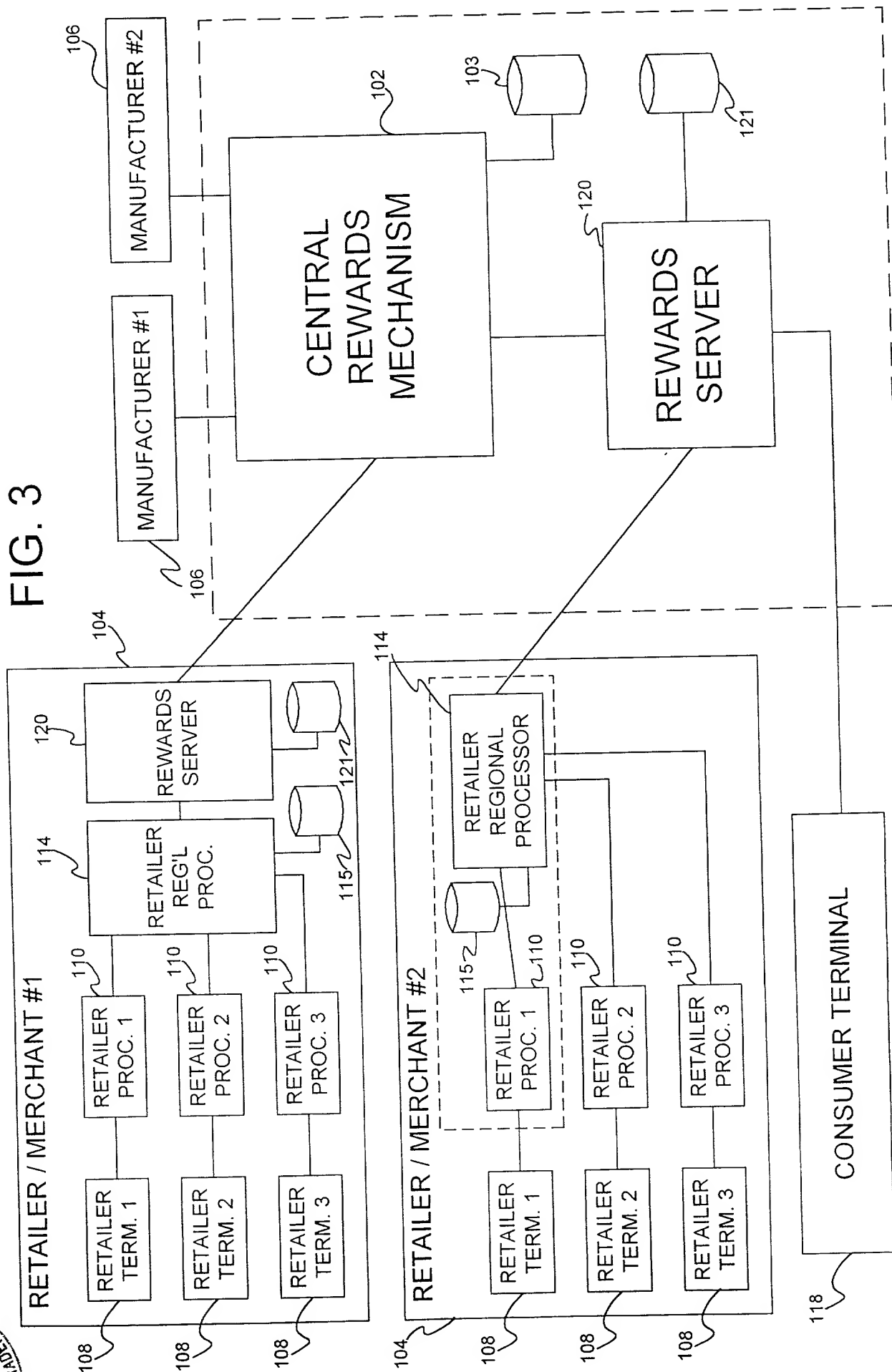


FIG. 3



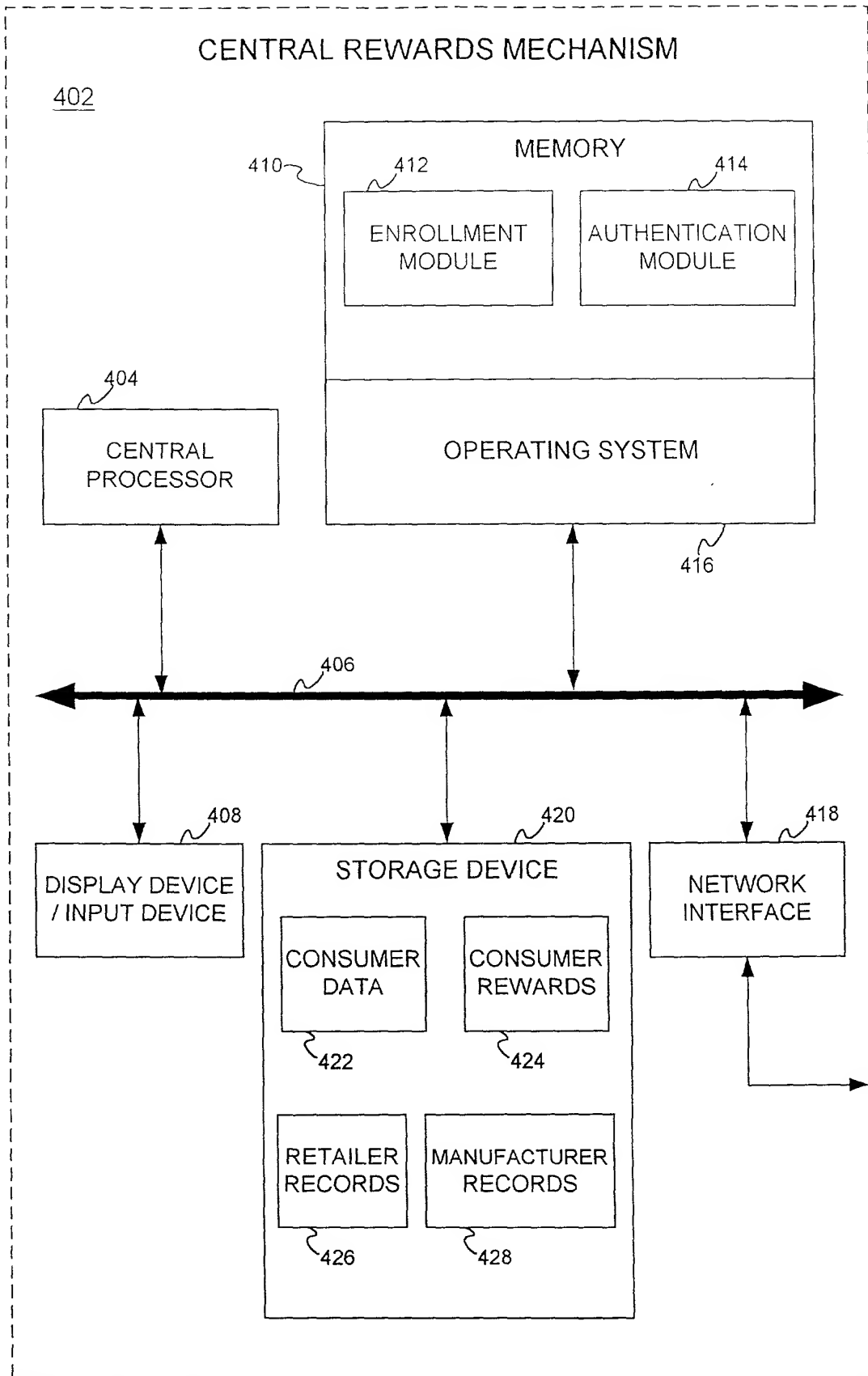


FIG. 4

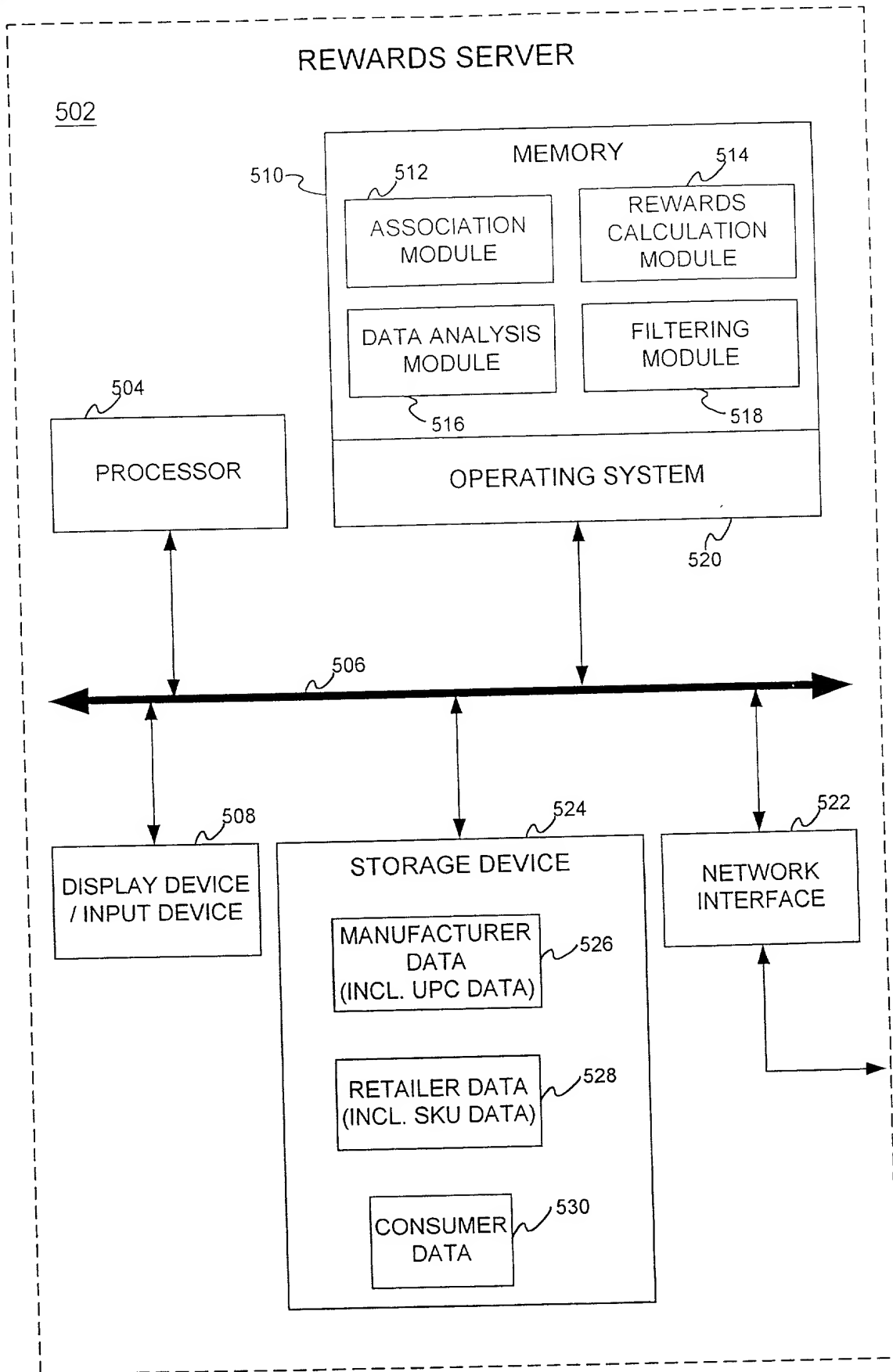


FIG. 5

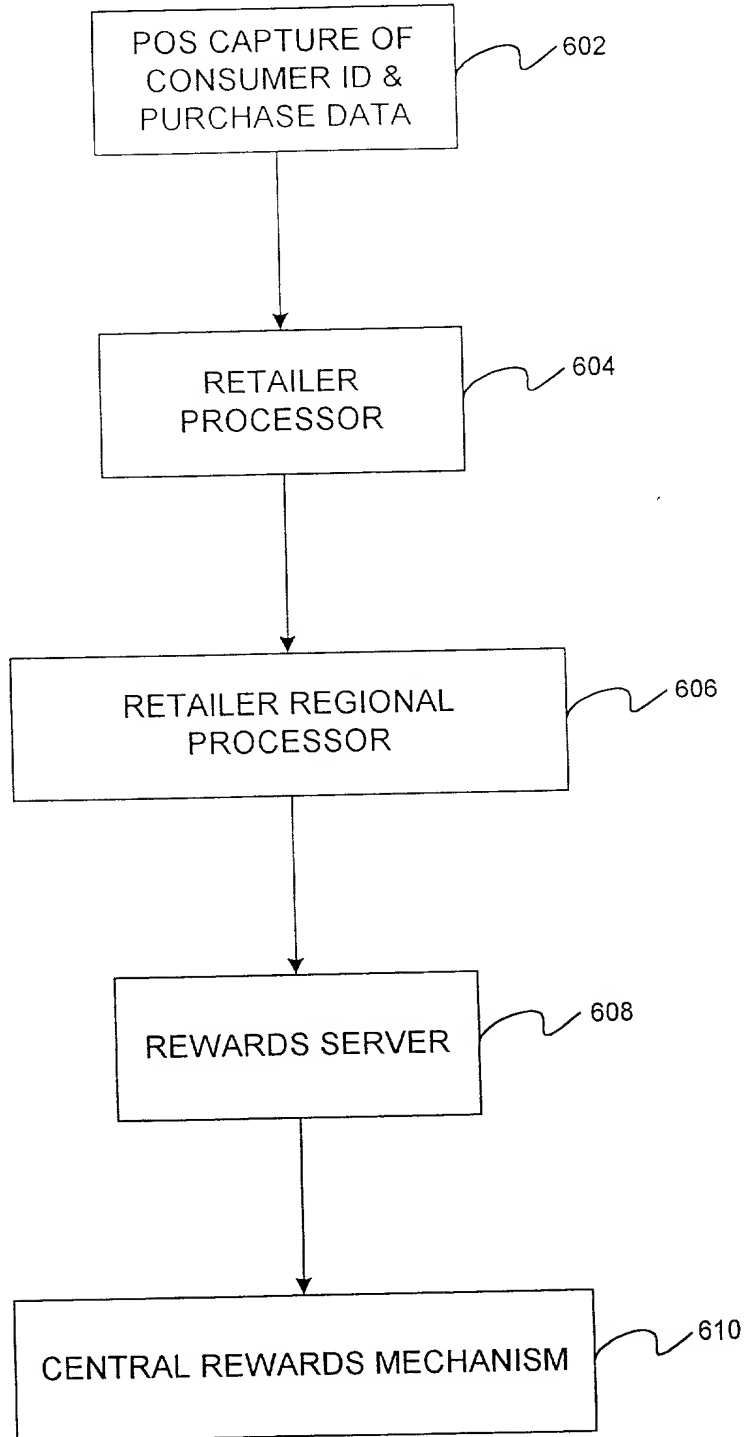


FIG. 6

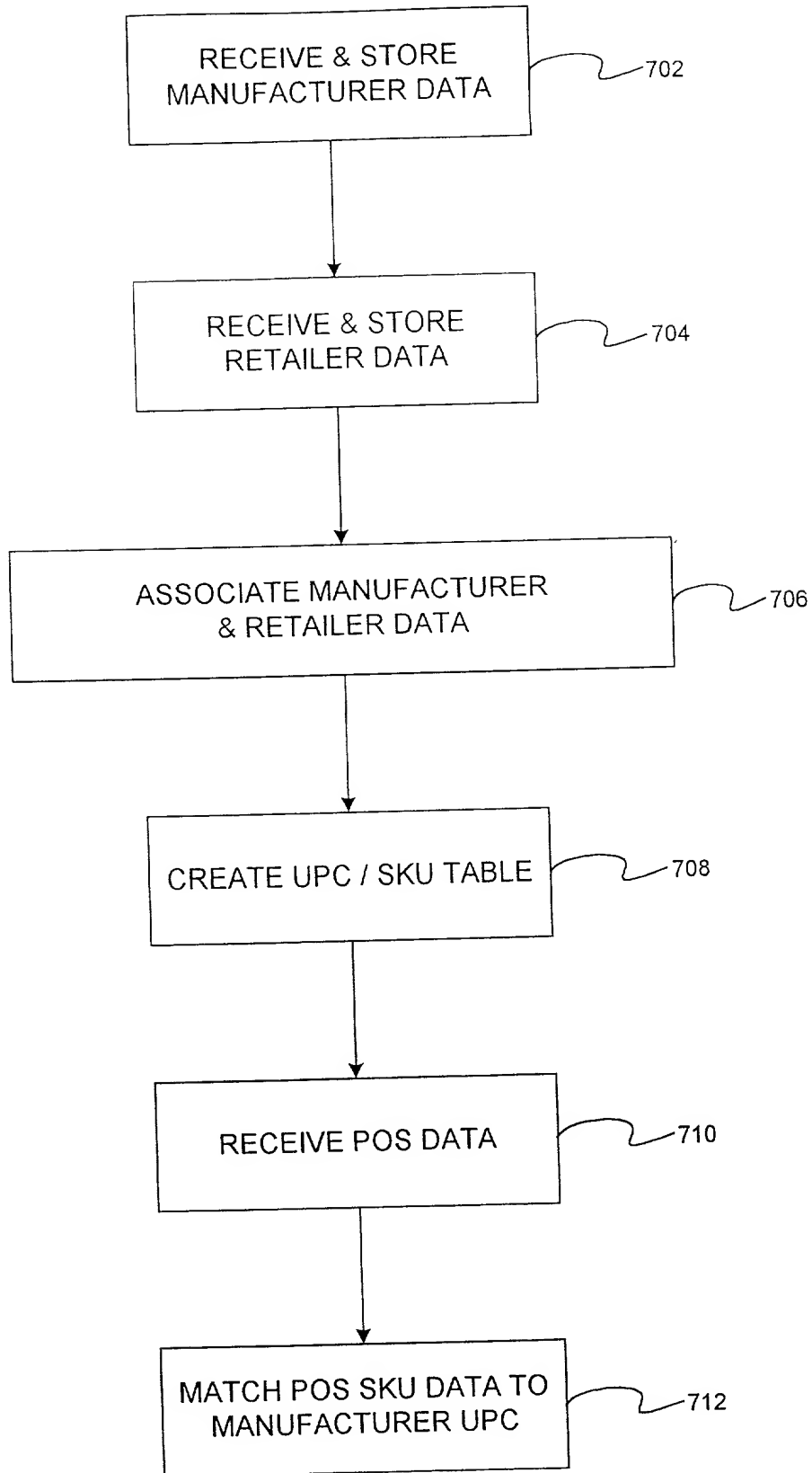


FIG. 7

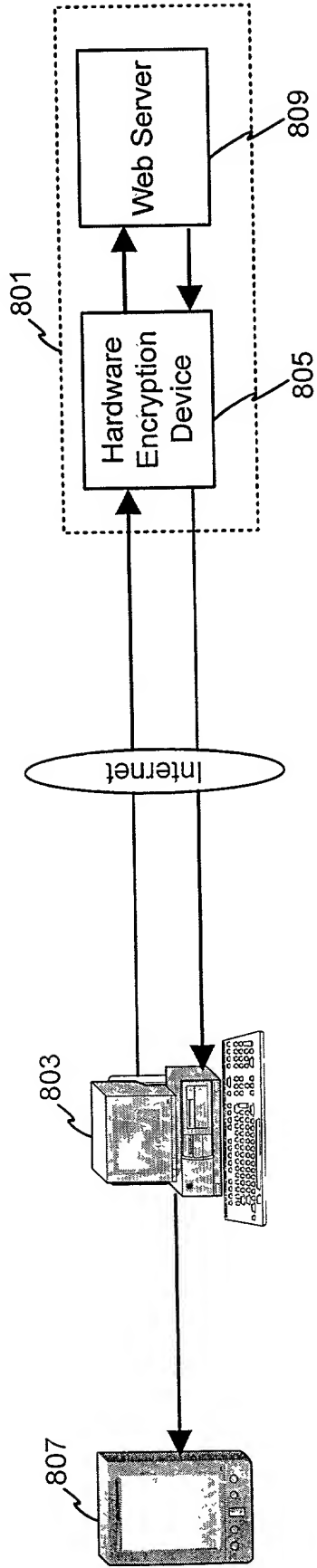


FIG. 8

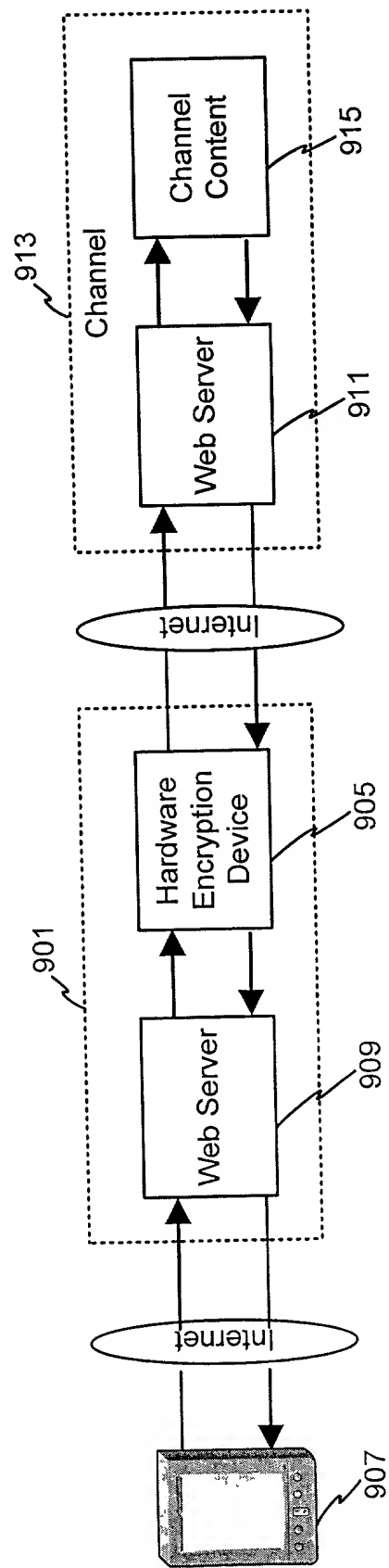


FIG. 9



FIG. 10

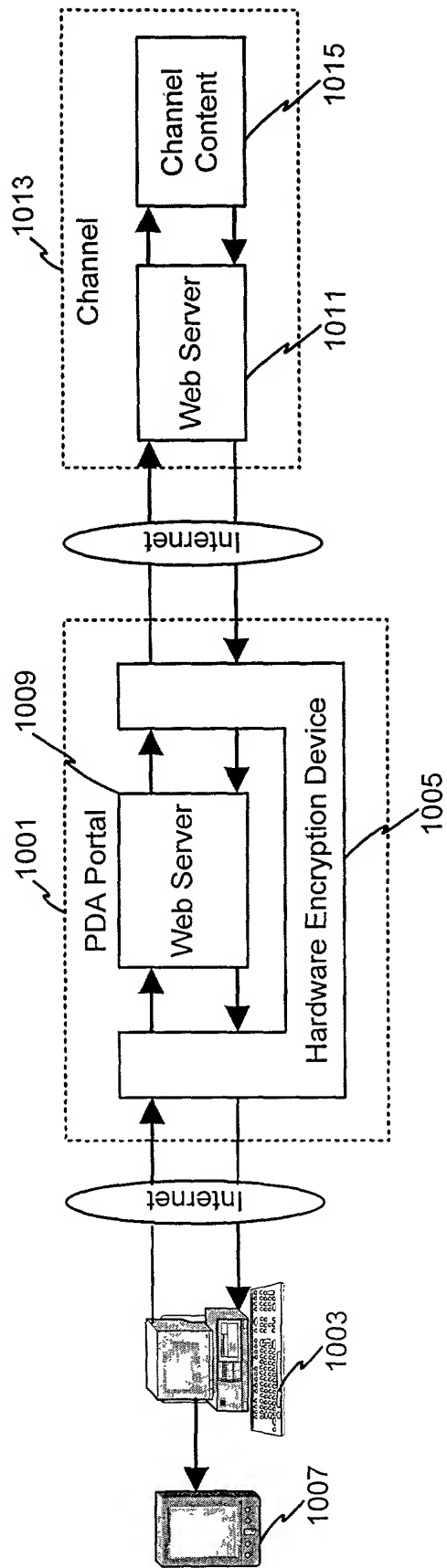


FIG. 10



FIG. 11 is a block diagram of a system architecture.

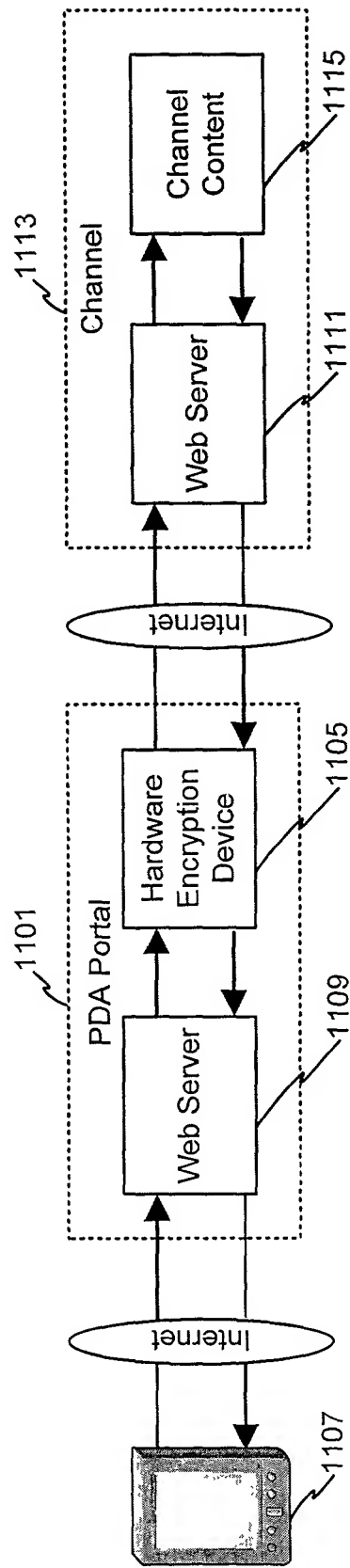


FIG. 11